

FACULTY PROFILE

1. **Name** : Sri. Syed Zahiruddin
2. **Date of Birth** : 05-03-1982
3. **Education Qualifications** : M. Tech.,
4. **Teaching Experience** : 12 Years



5. **Area of Specializations**
B.Tech. : Electronics and Communication Engg.,
M.Tech. : Digital Electronics and Computer Systems

Ph.D. : Pursuing in the domain of VLSI Design

6. Education Qualification:

Ph.D Pursuing in VLSI Design from Vignan University, Guntur.

Master of Technology in Digital Electronics and Computer Systems, from JNTU Anantapuram, First class with distinction, 2011.

Bachelor of Technology in ECE, from K.B.N College of Engg., V.T.U, Karnataka with First class, May, 2005.

7. Subjects Handled:

- VLSI Design
- Low Power VLSI
- Linear Integrated Circuits and Applications
- Probability theory and Stochastic Processes
- Analog Communications
- Microprocessors and its applications
- Digital Communications
- Electronic instruments and Measurements
- Electronic Devices and Circuits

8. List of Conferences

1. Syed Zahiruddin and Avireni Srinivasulu, "A High Frequency Tunable Sinusoidal Oscillator Using Single CCCII+" in proc. Of IEEE International Conference on Control, Instrumentation, Communication and Computational Technologies (ICCICCT), pp. 490-493. Dec (2015).
2. Syed Zahiruddin, "Reduction of ICI using ICI self cancellation scheme" International conference on Information Communication & Embedded Systems at S.A. Engineering college, Chennai, 23rd and 24th Feb, 2011.
3. Syed Zahiruddin and K.Ramanaidu, "Reduction of ICI using ICI self cancellation scheme in OFDM Systems" International Conference on Technology and Management ICTM 2011, Hyderabad on 8th and 9th June, 2011.

9. Workshops attended:

Name of the workshop	Place	Title	Month & Year	National / International
Two day workshop "National Science Day Celebration"	YVU, Kadapa	Emerging Trends In Academics And Research	Feb,2017	National
Two day hands on Training Program	JNTUA, Pulivendula	MSP 430 and TIVA Microcontrollers	July, 2016	National
Three day work shop	Vignan University, Guntur.	VLSI Design (Using cadence tool)	June, 2014	National
Two day workshop	K.S.R.M. College of Engineering, Kadapa	Graphical System design Platform Using LabVIEW	Jan, 2013	National
One Day National Workshop	K.S.R.M. College of Engineering, Kadapa	Transform Techniques in DSP: Theory & Applications	July, 2009	National
One day workshop	GKCE, Sullurpet	DW & DSP Processors	27-12-2008	National

10. List of Conferences and Workshops conducted:

S. No.	Conference / Workshop	Title	Date	Coordinators
1	National Level Students Technical Symposium	ENFEU-2K17 (Paper and Model Presentation, Video Presentation and Quiz	24-25 March, 2017	Sri S. Zahiruddin and Sri K. Pavan Kumar
2	Two day National level workshop	Internet of Things	25-26, March, 2016	Dr. M.V.Narayana and Sri S. Zahiruddin

11. Academics Administration:

- NBA ECE Coordinator.
- ECE BoS Member.

12. Additional Incharges:

- As a Time Table Incharge.
- As a Placement coordinator.
- As a Lab Incharge.
- As a Project Guide and involved in the preparation of Lab Manuals.