

FACULTY PROFILE

1. **Name** : Prof. P.V.S. Murali Krishna
2. **Date of Birth** : 10-06-1959
3. **Educational Qualifications** : .



- Pursuing Ph.D. (part-time) in Birla Institute of Technology (BIT), Mesra, Ranchi, Jharkhand State, India.
- Master of Engineering in Electronics and Communications Engineering from University of Roorkee (Presently it is IIT-Roorkee), Roorkee, U.P., 1985
- Bachelor of technology in Electronics and Communications Engineering from Jawaharlal Technological University, A.P., 1982

TRAINING

- In-house Training on C in CBS Inc., Chicago, USA (3 weeks)
- Training on Mainframes ES/9000 from Pentafor Software and Exports Limited, Chennai, India, 1995-96 (for 6 months)

4. **Teaching Experience** : 25+ Years
5. **Industrial Experience** : 4 Years :
6. **Training** : On Main Frames (6 Months)

7. Areas of Specializations

- B.Tech. : Electronics & Communication Engineering
M.Tech. : Control & Guidance
Ph.D. : VLSI

(A) Department of Compute Science and Engineering (CSE)-5 Years as Professor & HOD:

5 Years in KSRM College of Engineering, Kadapa

Subjects handled:

- Software Engineering
- Object Oriented Software Engineering
- Database Management Systems

- Data Structures
- Multimedia Systems
- Technical Communication and Computer Ethics
- Computer Organization

Laboratories:

- Database Systems
- Multimedia Systems
- Software Engineering

Projects for final year students: (12 Projects)

(B) Department of Electronics and Communications Engineering (ECE) 20 Years

Subjects Handled :

- Telephony
- Electronic Devices and Circuits
- Electronic Circuits Analysis
- Analog and digital ICs
- Microprocessors
- Pulse and digital Circuits
- Logic Circuits
- Switching Theory and Logic Design
- Communications Engineering
- TV Engineering
- Control Systems
- Computer Organization
- Computer Programming in Fortran-IV and Basic
- Linear IC Applications
- Electronic Instrumentation and Measurements

Laboratories:

All the laboratories related to above subjects except Microwave Engineering Lab

Projects for final year students:

More than 30 Projects

8. Journals & Conferences:

1. International Conference: “Current Conveyor based Cascadable Voltage Mode First Order All Pass Filter” presented paper in NAUN/IEEE.AM, International Conference, Peutro De La Cruz, Tenerife, SPAIN From Dec. 10-12, 2011

2. “Two new All Pass Filters based on a Single Differential Difference Complementary Current Conveyor”, 2012 International Conference on Applied Electronics, Pilsen, Chechoslovakia, 5th to 7th Sep. 2012 .
3. “Second Generation Current Conveyor based Square Wave Generator”, P.V.S. Murali Krishna, Avireni Srinivasulu, Sanhita Manna and Sweta Kaundilya, R.K. Lal, BIT International Conclave on”Innovations in Engineering & Management (ICEM 2012), 22nd-23rd Feb. 2013,BIT, Patna, Bihar, India
4. Two New First Order All Pass Filters Based On Differential Difference Complementary Current Conveyor, International Journal of Control Theory and Applications, vol. 8, no. 1, ISSN 0974-5572, Jun. 2015.
5. Grounded Resistor/Capacitor Controlled Multi-phase Sinusoidal Oscillator Using DDCCCs Naveen Kumar, P.V.S.M Krishna, R.K.Lal and Avireni Srinivasulu, International Conference, 6th to 7th April 2016 (INSPEC Accession Number: 16498086) Adiparashakti Engineering College, Memaruvattur, Tamil Nadu

12. List of Conferences and Workshops conducted in K.S.R.M. College of Engineering, Kadapa as Convener:

S. No.	Conference / Workshop	Title	Date	Coordinators
1	National Level Workshop	Transform Techniques in DSP: Theory and Applications	04-07-2009	Sri M.V.Narayana
2	National Level Students Technical Symposium	ELICITE-2K11 (Paper and Poster Presentation, Project Design and Circuit Debugging)	28-10-2011	Sri M.V.Narayana and Sri R.V.Sree Hari
3	National level Two day Workshop	Graphical System Design Platform using LabView	4 th to 5 th , January 2013	Sri M.V.Narayana and Sri Md Mahaboob Pasha
4	Two day National level Technical symposium	Evince2k14 (Paper,Poster,and Project presentations, Technical quiz, Circuit debugging, non-technical events)	11-12, April, 2014	Dr. M.V. Narayana and Sri Md. Mahaboob Pasha
5	Two day National level workshop	Internet of Things	25 th -26 th , March, 2016	Dr. M.V.Narayana and S.Zahiruddin

13. ADMINISTRATIVE EXPERIENCE

(a) Roles performed in KSRM College of Engineering, Kadapa:

- Worked as in-charge for the Internet in the College
- In-charge for Office Automation software
- Coordinator for College Website
- Main in-charge for cultural activities and competitions in Decennial Celebrations (1990) and Silver Jubilee Celebrations (2005) of KSRM College of Engineering
- Convener for workshops organized in KSRM College of Engineering
- Head of the CSE/IT Department for 5 Years (2002-2007)
- Head of the ECE Department from 2007 till date
- Coordinator for NBA And AICTE activities

(b) Other Areas:

- Served as senior software Consultant in Detroit / Chicago for 1.5 Years
- Gave a seminar (one day session) on 'IT Service Management' for senior colleagues in HCL Perot Systems, Bangalore in the year of 2001
- Gave one guest lectures in JNTU college of Engineering, and 2 Engineering colleges affiliated to S.V. University
- Gave a lecture on 'Latest trends in IT' on the Engineers day organized by the Institute of Engineers, India
- Attended a summer school on 'Parallel Processing' in Indian Institute of Science (IISC), Bangalore
- Appointed as guide for more than 40 projects for AMIE students by Institution of Engineers, Kolkatta
- In-charge and training faculty for the certificate Course on 'Fortran IV' organized by the S.V. University in KSRM College of Engineering Kadapa in late 80's
- Developed the code of conduct (Ethical policy) for KSRM college of engineering It is being used in our college and in some other colleges

- Acted as external Lab examiner for University lab exams in many Engineering Colleges.
- Acted as resource person for an International Conference in BIT, Patna

Ph.D. Title: Studies on Some Differential Difference Current Conveyor (DDCC) / Second Generation Current Conveyor (CCII) Based All-pass Filters / Oscillators

Dissertation work in Master of Engineering: 'Design of wave digital filters' using FORTRAN 77 on DEC-20 System

Project work in Bachelor of Technology: 'Simulation of elevator' using Microprocessor 8085 and Assembly Language Programming

(c) Achievements:

- Was awarded National Merit Scholarship for 7 years (Intermediate Course onwards till the end of B.Tech.)
- Have 1 ½ years of experience as Senior Consultant in CBS Inc., USA (from Apr 1988 to Oct. 1999)
- Have 9 months of experience as Project Manager in HCL Perot Systems, Bangalore (From Dec. 2000 to Aug. 2001)
- Worked in CMM Level-5 IT organizations
- Learned thoroughly the Software Engineering principles (As a Project Manager in HCL Perot Systems) and Faculty member in KSRRM Engineering college

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